

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	Examiner: Osborne, Luke R.
)	
Kenneth Y. Ogami)	Group Art Unit: 2123
)	
Application No.: 09/989,762)	Confirmation No.: 2087
)	
Filed: November 19, 2001)	
)	
For: SYSTEM AND METHOD FOR)	
PERFORMING NEXT)	
PLACEMENTS AND PRUNING OF		
DISALLOWED PLACEMENTS FOR		
PROGRAMMING AN		
INTEGRATED CIRCUIT		

ISSUE FEE TRANSMITTAL

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant is in receipt of a Notice of Allowance and Fee Due Form, mailed September 16, 2010, in connection with the above-identified application. The Issue Fee is due on or before December 16, 2010. Applicant hereby submits the Issue Fee and/or the Publication Fee.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17, 1.18, 1.19, 1.20 and 1.21 that may be required to issue the present application, and to credit any overpayments, to Deposit Account No. 50-3781.

/

/

/


Customer No.: 60909

Should the Patent Office have any questions regarding this submission or the application in general, the Patent Office is urged to contact the Applicant's attorney, Larry Johnson, by telephone at (408) 545-7194. All correspondence should continue to be directed to the address given below.

Respectfully submitted,

Dated: 9/20/10

By: _____


Larry Johnson
Attorney for Applicant
Registration No. 56,861

Cypress Semiconductor Corporation
198 Champion Court
San Jose, CA 95134
Facsimile: (408) 545-6911
Customer No.: 60909

Customer No.: 60909